



The Time of Flight detector of the AMS-02: commanding programs and monitoring tools

Lecture 3

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for TOF collaboration*



Outline

- The TOF software tools
- TOF commanding programs
- Software to configure the TOF parameters
- TOF online monitor
- Calibration, Configuration and house keeping monitors
- TOF physical event monitor
- Errors monitor
- TOF slow control monitors

TOF software tools

TOF is configured and monitored using several software.

Commanding/Configuration:

- BBtool is a software used to communicate and send commands to S-crates.
- SDR2, SPT2, SFET2, Brick command interfaces are used to send direct commands to TOF boards and SHV bricks.
- AMSTOF_configuration used to generate TOF configuration file in a format compatible with the DSP program.

Monitor:

- Slow control

PDS-M, JLV1-M, SPT2-M and TOF-DTS-M are used to monitor TOF input currents, trigger signals and temperatures...

- DAQ

TOF online monitor is used during data acquisition to verify TOF configuration and data quality.

Commanding of the TOF electronics

BBdaq (Bare Bones DAQ)

This software is used to perform various low level operations with the TOF electronics.

Boot the node, get the status ...

Write/read/load/delete a DSP program or configuration file in the memory flash, retrieve the house keeping information, the configuration...and other useful Sub-Detector Procedures have been implemented for the TOF.

In BBdaq it's possible to use macro to execute sequentially commands addressed to different nodes.

```
DAQ>> sdr 0 a
DAQ/SDR2-0-A (CMD FILE)>> ?
```

SDR-2 Menu

1	[BOOT]	Boot the node
2	[STATUS]	Get the node status
3	[PING]	Ping the node
4	[PURDOWN]	Enter power down
5	[FLASHRD]	Read a file from FLASH
6	[FLASHWR]	Write a file to FLASH
7	[FLASHTST]	Test a file in the FLASH
8	[FLASHLD]	Load a FLASH file
9	[FLASHDIR]	Read FLASH summary
10	[FLASHERASE]	Flash file/sector erase
11	[FLASHDF]	File Attribute Set
12	[-]	-
13	[HKINFORD]	Read HK info
14	[CALSTATUS]	Read Calibration Status
15	[CALCNTRL]	Perform Calibration
16	[SDPRORD]	SD procedure status
17	[-]	-
18	[-]	-
19	[-]	-
20	[WAIT]	Let the program wait
21	[RETURN]	Back to DAQ Menu

```
DAQ/SDR2-0-A (CMD FILE)>> █
```

Commanding of the TOF electronics

Slow control SDR2, SPT2, SFET2/SFEA2, HVBrick controllers.

These graphical interfaces are used to send specific AMSwire commands directly to TOF & ACC nodes, to set and read back a register or a group of registers value.

HV Brick Controller - 4 (TOF)

LR Group	Chan	Write Volts	Read Volts	Reset
0	R 0	7 W	R RESET	
0	R 1	7 W	R RESET	
0	R 2	7 W	R RESET	
0	R 3	7 W	R RESET	
0	R 4	7 W	R RESET	
0	R 5	7 W	R RESET	
0	R 6	7 W	R RESET	
0	R 7	7 W	R RESET	
1	R 8	7 W	R RESET	
1	R 9	7 W	R RESET	
1	R 10	7 W	R RESET	
1	R 11	7 W	R RESET	
1	R 12	7 W	R RESET	
1	R 13	7 W	R RESET	
1	R 14	7 W	R RESET	
1	R 15	7 W	R RESET	
2	R 16	7 W	R RESET	
2	R 17	7 W	R RESET	
2	R 18	7 W	R RESET	
2	R 19	7 W	R RESET	
2	R 20	7 W	R RESET	
2	R 21	7 W	R RESET	
2	R 22	7 W	R RESET	
2	R 23	7 W	R RESET	
	ALL	WRITE	READ	RESET

Buttons: READ ALL, WRITE ALL, UPDATE INPUT, RESET ALL, SHUT DOWN

SPT2 Pro Controller

Power: ON OFF R

Int. Generator: 0 0 0 0 0 0 W R ON OFF R TRIG

Pretrigger Setup: 0 CP CT S2 0 HT SHT 0 HT SHT 0 HT SHT

States: 0 HT SHT 0 HT SHT 0 HT SHT

Signal Patterns: 0 HT SHT 0 HT SHT 0 HT SHT

Scalars: High Threshold Super High Threshold (32)

Buttons: READ, WRITE, UPDATE INPUT

SFET2/SFEA2 - 3 Controller

SFET2-A, SFET2-B, SFET2-C, SFET2-D, SFEA2

Power: ON OFF R

Ch Thr. mV Code Vref

0 1 2 LT HT SHT

3 4 LT HT SHT

Buttons: WRITE, READ, READ ALL

SDR2 Controller

Power: ON OFF R

Programmable Read Time: 0.00 0.01 0.02 0.05 0.1 0.2 0.5 1 2 5 10 20 50 100 200 500 1000

Buttons: GET FLASH SUMMARY, GET STATUS

SC Status: SC Status, SC Error, SC Error Code

Board Temperature: Board Temperature

Power Status (New): Power Status (New)

Initialization Status (New): Initialization Status (New)

Commanding of the SDR2

A. Lebedev 07-Aug-10

SDR2 Controller

EPP: 2 SDR2 Path: 1 3 3 F Crate: SZB S/W: New

 Programmable Dead Time us: 0.00 W R EPP: 0 0 TRIG CAN BUSY

 Processing Mode: Off W R Last Processed Event Nb: R

Vers	Pgm	Node	Node Type	Ports to M	SubD Vers	Errors							ANSM Link	Time sec	Last Event#	Bldg. Errors	ANSw Errors	Flash Errors	<input type="button" value="GET STATUS"/>
						E	A	F	B	P	D	T							

Initialisation Status (New)			
1	S-Crate Status Word		Pwr Param
2	SDR2 Error Code	SDR2	
4	SFET2-A Error Code	SFET2-A	
5	SFET2-B Error Code	SFET2-B	
3	SPT2 Error Code	SPT2	
6	SFET2-C Error Code	SFET2-C	
7	SFET2-D Error Code	SFET2-D	
8	SFEA2 Error Code	SFEA2	
9	SHV Status Word	SFEC	
10	SHV Error Code	SFEC c1k	SPT2 trng
<input type="button" value="READ"/>			

Power Control		Actel Version ID		Board Temperature		Power Status (New)					
	H Ctr1	H Ctr1	Mon	Stat	SDR2			Se1	Sta	Ctr1	Actel II
SFET2-A	E OFF				SFET2-A						
SFET2-B	E OFF				SFET2-B						
SPT2	E OFF				SPT2						
SFET2-C	E OFF				SFET2-C						
SFET2-D	E OFF				SFET2-D						
SFEA2	E OFF				SFEA2						
SFEC	E OFF				SFEC						
Parity: 0		Cnd: W R		<input type="button" value="READ"/>		<input type="button" value="READ"/>		Cnd: ON OFF		<input type="button" value="READ"/>	

S/H Delay us		ADC Delay us		ADC Clock		SFET/A2 Timeout		Max Event Size	
	0.00	W		R		0.00	W		R
	0.00	W		R		0	W		R
	0.00	W		R		0.00	W		R
	0	W		R		0	W		R

Commanding the SFET2 and SFEA2

A. Lebedev 23-Oct-09

EPP	Port	SDR2 Path	Crate	S/W	SFET2/SFEA2 - 3 Controller									
	2	0 0 0 0	N/A	New										

SFET2-A		Actel ID		SFET2-B		Actel ID		SFET2-C		Actel ID		SFET2-D		Actel ID		SFEA2		Actel ID					
Power	ON OFF		R	Power	ON OFF		R	Power	ON OFF		R	Power	ON OFF		R	Power	ON OFF		R				
Ch	Thr	mV	code	Vref	Ch	Thr	mV	code	Vref	Ch	Thr	mV	code	Vref	Ch	Thr	mV	code	Vref				
0 1 2	LT	0	0	INT	W	0 1 2	LT	0	0	INT	W	0 1 2	LT	0	0	INT	W	0 1	LT	0	0	INT	W
	HT	0	0	INT	W		HT	0	0	INT	W		HT	0	0	INT	W		N/A		HT	N/A	
	SHT	0	0	INT	W		SHT	0	0	INT	W		SHT	0	0	INT	W		N/A		SHT	N/A	
3 4	LT	0	0	INT	W	3 4	LT	0	0	INT	W	3 4	LT	0	0	INT	W	2 3	LT	0	0	INT	W
	HT	0	0	INT	W		HT	0	0	INT	W		HT	0	0	INT	W		N/A		HT	N/A	
	SHT	0	0	INT	W		SHT	0	0	INT	W		SHT	0	0	INT	W		N/A		SHT	N/A	
Cmd	WRITE		READ		Cmd	WRITE		READ		Cmd	WRITE		READ		Cmd	WRITE		READ					
Trigger Delay, us	0.00		W		Trigger Delay, us	0.00		W		Trigger Delay, us	0.00		W		Trigger Delay, us	0.00		W					
FT to reset FE Chip	ENA	DIS		R	FT to reset FE Chip	ENA	DIS		R	FT to reset FE Chip	ENA	DIS		R	FT to reset FE Chip	ENA	DIS		R				
Temperature, C			R		Temperature, C			R		Temperature, C			R		Temperature, C			R					
READ ALL				READ ALL				READ ALL				READ ALL				READ ALL							
READ ALL																							

P 0

Commanding the SPT2

EPP

Port
2

SDR2 Path
0 0 0 0

Crate
N/A

S/W
New

A. Lebedev 29-Jul-09

SPT2 Pro Controller

Power **ON** OFF R

Int. Generator 0 0 0 0 0 W R ON OFF R TRIG

Gate 0.25s W R

Pretrigger Setup				"States"			Signal Patterns						Scalers				
0	CP	CT	BZ	0	HT	SHT	0	HT	SHT	0	Masked			High Threshold		Super High Threshold(BZ)	
									CP		CT	BZ					
0	D	D	D	0			0			0					0		R
1	D	D	D	1			1			1					1		R
2	D	D	D	2			2			2					2		R
3	D	D	D	3			3			3					3		R
4	D	D	D	4			4			4					4		R
5	D	D	D	5			5			5					5		R
6	D	D	D	6			6			6					6		R
7	D	D	D	7			7			7					7		R
8	D	D	D	8			8			8					8		R
9	D	D	D	9			9			9					9		R
Gen				Gen			Gen			Gen			Internal Generator				
Ext				Ext			Ext			Ext			Int				
WRWRWR				R R			R R			R R R R			READ READ				
												READ					
1	CP	CT	BZ	1	HT	SHT	1	HT	SHT	1	Masked			High Threshold		Super High Threshold(BZ)	
0	D	D	D	0			0			0					0		R
1	D	D	D	1			1			1					1		R
2	D	D	D	2			2			2					2		R
3	D	D	D	3			3			3					3		R
4	D	D	D	4			4			4					4		R
5	D	D	D	5			5			5					5		R
6	D	D	D	6			6			6					6		R
7	D	D	D	7			7			7					7		R
8	D	D	D	8			8			8					8		R
9	D	D	D	9			9			9					9		R
Gen				Gen			Gen			Gen			Internal Generator				
Ext				Ext			Ext			Ext			Int				
WRWRWR				R R			R R			R R R R			READ READ				
												READ					

Pretrigger Output

DISABLE

WRITE **READ**

Config File

WRITE | **READ**

READ ALL

WRITE ALL

UPDATE INPUT

Commanding the HV-brick

A. Lebedev 26-Jul-10

HV Brick Controller - 4 (TOF)

EPP: 1 Port: 1 SDR2 Path: 0 0 0 0

ECAL RICH TOF Bus: 0 GEO: 0 1 INIT LeCroy Bus: GEO: FIND Brick

User

ACC&TOF

LR Group	Chan	Write		Read		Reset
		Volts		Volts		
0	LR 0	7	W		R	RESET
	LR 1	7	W		R	RESET
	LR 2	7	W		R	RESET
	LR 3	7	W		R	RESET
	LR 4	7	W		R	RESET
	LR 5	7	W		R	RESET
	LR 6	7	W		R	RESET
	LR 7	7	W		R	RESET
1	LR 8	7	W		R	RESET
	LR 9	7	W		R	RESET
	LR 10	7	W		R	RESET
	LR 11	7	W		R	RESET
	LR 12	7	W		R	RESET
	LR 13	7	W		R	RESET
	LR 14	7	W		R	RESET
	LR 15	7	W		R	RESET
2	LR 16	7	W		R	RESET
	LR 17	7	W		R	RESET
	LR 18	7	W		R	RESET
	LR 19	7	W		R	RESET
	LR 20	7	W		R	RESET
	LR 21	7	W		R	RESET
	LR 22	7	W		R	RESET
	LR 23	7	W		R	RESET
All		WRITE		READ	RESET	
DCDC-0		7	W		R	RESET
		DCDC OFF	W			

Error Readout							
0							R
1							R
2							R
3							R
4							R
5							R
READ							

XXX-HV-01 W

XXX-HV-01 R

READ ALL

WRITE ALL

UPDATE INPUT

RESET ALL

SHUT DOWN

P 0

Parameters of the TOF electronics

AMSTOF_configuration http://g5.ambra.unibo.it/AMSTOF_configuration/index.php

This program is used to generate TOF and ACC configuration file in the DSP format containing all the parameters for the TOF electronics.

This software written in php is installed in a web server and accessible through internet from any sites (username and password are required). An user friendly interface allows to change the key parameters for the tuning of the detector.

All the generated configuration files are stored in the disc so they can be reused in a second moment to reconfigure the detector or to check the used configuration of the detector in a specific date.



SDR configuration



Home Logout Change password Help

Set Crates:
S0 (1n & 2p)
S1 (1p & 2n)
S2 (3p & 4p)
S3 (3n & 4n)

Conf. file list:
S0 (1n & 2p)
S1 (1p & 2n)
S2 (3p & 4p)
S3 (3n & 4n)

Crate S0

Select file

Default Configuration

Description

Created by: Bindl, on 01/09/10 at 22:42:14
Comment: TOF&ACC KSC SHT 80

High Voltages		Thresholds			Other parameters		
101n1:	1801	102n+103n+105n:	LT	HT	SHT	DC/DC power:	1
101n2 + 108n2:	1801	107n:	8	10	80	SFEC Clock Enable:	1
101n3 + 108n1:	1801	102n+104n+106n:	8	10	80	LVDS driver enable:	1
102n1 + 107n2:	1950	108n:	8	10	80	Prescaler gate:	2
102n2 + 107n1:	2001	202p+203p+205p:	8	10	80	Mask for CPE:	3072
103n1 + 106n3:	1750	207n:	8	10	80	Mask for C00:	1072
103n2 + 106n1:	1750	202p+204p+206p:	8	10	80	Mask for CT1:	3072
104n1 + 105n2:	1950	208p:	8	10	80	Mask for C70:	3072
104n2 + 105n1:	1950	ACC01+ACC02+ACC03:	200	200	200	Mask for BZ1:	3072
108n3:	2001	ACC04:	200	200	200	Mask for BZ0:	3072
201p1 + 208p2:	2249	Data Processing Control			Pulser Control:	0	
201p2 + 208p1:	2249	Dynamic Pedestal Control:			Pulser Period:	0	
202p1 + 207p2:	2150	Naligma for SFET threshold:	1		Programmable BUSY:	170	
202p2 + 207p1:	2101	Naligma for SFEA threshold:	5				
203p1 + 206p2:	2001	Naligma for SFEA threshold:	3				
203p2 + 206p1:	2001	Naligma for SFEC threshold:	4				
204p1:	1701	Low limit on SFET threshold:	48				
204p2:	1701	High limit on SFET threshold:	120				
205p1:	1701	Low limit on SFEA threshold:	96				
205p2:	1701	High limit on SFEA threshold:	224				
ACC01:	1981	Low limit on SFEC threshold:	80				
ACC02:	1957	High limit on SFEC threshold:	160				
ACC03:	1994						
ACC04:	1957						
DC/DC setting:	2349						

Create configuration file

TOF online monitor

DAQ online monitor software allows to monitor and check TOF data quality.

The program reads AMS block data, decodes them, extracts all the data related to TOF and plots the most significant information.

The program processes each data file, reads all AMS blocks and extracts all S-crate information contained inside JMDC blocks. All retrieved information can be dumped in a log file (mainly for debugging purpose). Depending on data type, physics data, configuration parameters, calibration results and DAQ housekeeping words are extracted for each S-crate.

While the monitor is running, the processed data files are showed in the terminal window.

CALIBRATION, CONFIGURATION, DAQ HOUSEKEEPING and ERROR files are created and stored inside the proper directories.



The terminal window displays the following output:

```
File Edit View Bookmarks Settings Help
writing calibration data into file: /home/quadrani/TOF/Monitor2011/cal/1290019917.cal
writing errors into file: /home/quadrani/TOF/Monitor2011/err/1290019917.err
../Data/BLOCKS/HRDL-A/0180/696
Empty ERROR file removed.
writing configuration data into file: /home/quadrani/TOF/Monitor2011/conf/1290019918.conf
writing errors into file: /home/quadrani/TOF/Monitor2011/err/1290019918.err
Empty ERROR file removed.
writing DAQ housekeeping data into file: /home/quadrani/TOF/Monitor2011/daqhk/1290019919.hk
writing errors into file: /home/quadrani/TOF/Monitor2011/err/1290019919.err
Crate S1: DAQ HouseKeeping OK
Crate S0: DAQ HouseKeeping OK
Crate S2: DAQ HouseKeeping OK
Crate S3: DAQ HouseKeeping OK
Empty ERROR file removed.
writing errors into file: /home/quadrani/TOF/Monitor2011/err/1290019920.err
../Data/BLOCKS/HRDL-A/0180/697
../Data/BLOCKS/HRDL-A/0180/698
Run = 1290019920 ev 53358    T4 status: 9010    Error: No room to append BC=11 packet
Run = 1290019920 ev 53358    T5 status: 9011    Error: No room to append BC=11 packet
Run = 1290019920 ev 53358    S2B status: 9013   Error: No room to append BC=11 packet
Run = 1290019920 ev 53358    T6 status: 9016    Error: No room to append BC=11 packet
../Data/BLOCKS/HRDL-A/0180/699
```

Annotations with arrows pointing to the terminal output:

- Calibration file**: points to the line `writing calibration data into file: /home/quadrani/TOF/Monitor2011/cal/1290019917.cal`
- Configuration file**: points to the line `writing configuration data into file: /home/quadrani/TOF/Monitor2011/conf/1290019918.conf`
- Housekeeping file**: points to the line `writing DAQ housekeeping data into file: /home/quadrani/TOF/Monitor2011/daqhk/1290019919.hk`
- Processed file**: points to the line `../Data/BLOCKS/HRDL-A/0180/697`
- Error file**: points to the line `writing errors into file: /home/quadrani/TOF/Monitor2011/err/1290019920.err`

The terminal window title bar shows "Monitor2011 : bash".

TOF calibration monitor

Calibration (data type 6)

S-crate calibration results are available at the beginning of each run (every 30 minutes) and

TOF monitor saves them in a text file:

- 1 calibration status word;
- 90 pedestal values;
- 90 pedestal widths
- 90 thresholds values;
- 4 pre-trigger words;
- 10 status words.

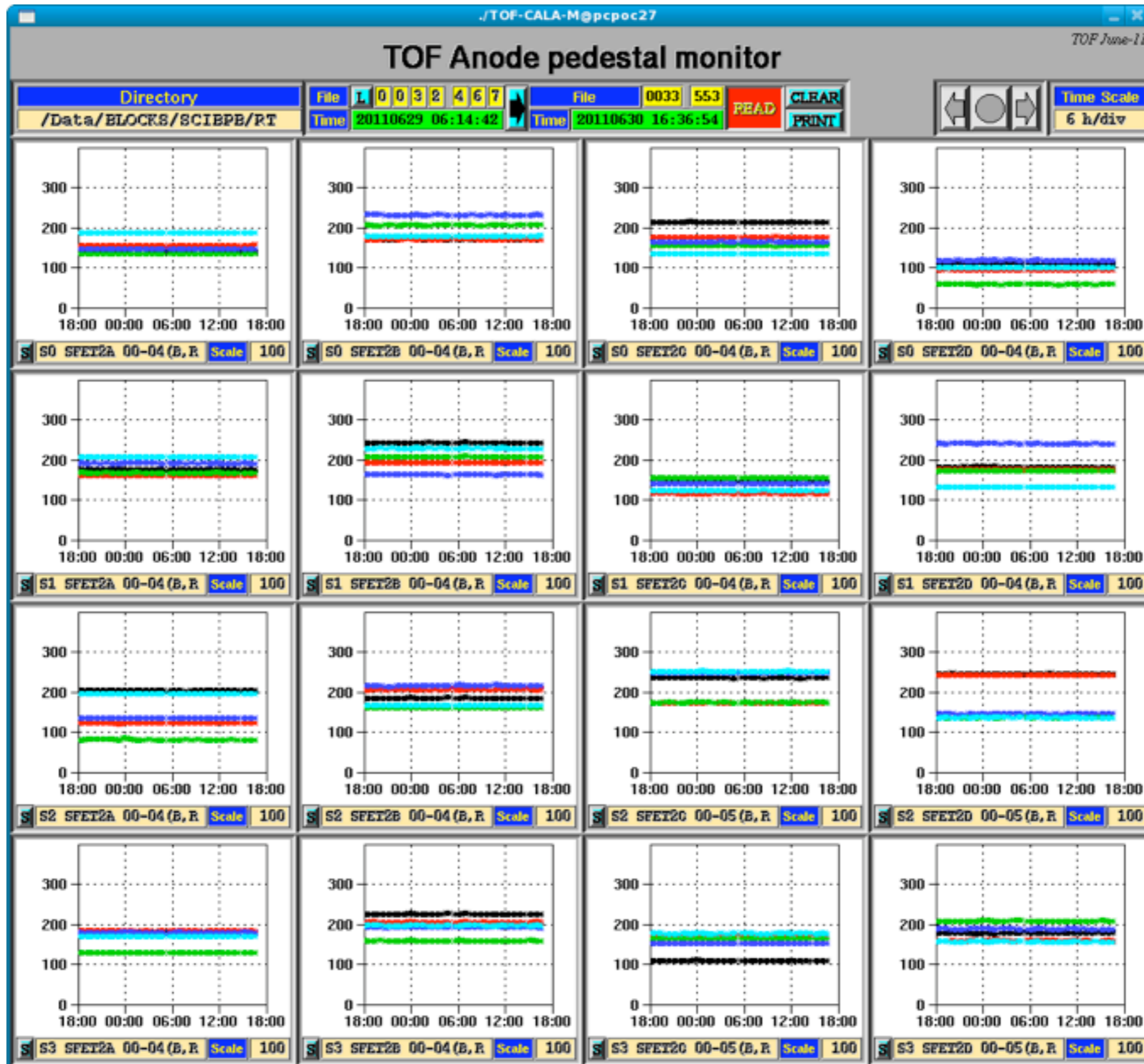
```
Crate S0
Claibration Status = 4000
Calibration results are available.

SFET2A
pede:      205.6 100.8 159.4 102.2 136.1 167.9 137.1 125.5 148.2 147.4
width:     1.5  1.4  1.2  1.4  1.4  0.9  0.9  0.9  0.9  0.9
thr:       7.5  6.9  6.2  6.9  6.9  6.0  6.0  6.0  6.0  6.0
SFET2B
pede:      97.8 240.5 227.0 249.8 191.5 132.6 284.0 217.6 256.5 186.9
width:     1.5  1.4  1.2  1.4  1.4  0.9  0.9  0.9  0.9  0.9
thr:       6.9  6.9  8.8  6.9  6.2  6.0  6.0  6.0  6.0  6.0
SFET2C
pede:      198.5 240.6 236.6 151.4 137.0 206.9 181.1 179.2 181.9 212.8
width:     1.5  1.4  1.2  1.4  1.4  0.9  0.9  0.9  0.9  0.9
thr:       7.5  6.2  6.2 15.0  7.5  6.0  6.0  6.0  6.0  6.0
SFET2D
pede:      110.4 198.4 180.6 145.4 247.8 178.6 204.1 199.2 219.9 205.4
width:     1.5  1.4  1.2  1.4  1.4  0.9  0.9  0.9  0.9  0.9
thr:       9.4  6.2  6.2  7.5  6.2  6.0  6.0  6.0  6.0  6.0
SFEA2
pede:      176.8 148.6 247.0 190.4 193.1 182.1 157.6 164.5 248.4 206.1
width:     1.5  1.4  1.2  1.4  1.4  0.9  0.9  0.9  0.9  0.9
thr:       21.0 25.5 28.0 18.8 12.0 12.0 12.0 12.0 12.0 12.0
SFEC00
pede:      530.0 482.0 251.4 408.6 322.0 211.1 251.0 294.5 494.0 338.1
width:     1.5  1.4  1.2  1.4  1.4  0.9  0.9  0.9  0.9  0.9
thr:      10.0 10.0 10.0 10.0 10.0 10.0 10.0 10.0 10.0 10.0
SFEC01
pede:      330.9 366.0 274.0 401.8 453.4 368.8 478.5 661.9 238.0 73.9
width:     1.5  1.4  1.2  1.4  1.4  0.9  0.9  0.9  0.9  0.9
thr:      10.0 10.0 10.0 10.0 10.0 10.0 10.0 10.0 10.0 10.0
SFEC10
pede:      50.8 285.4 407.2 545.2 304.6 443.6 513.6 404.1 552.9 357.4
width:     1.5  1.4  1.2  1.4  1.4  0.9  0.9  0.9  0.9  0.9
thr:      10.0 10.0 10.0 10.0 10.0 10.0 10.0 10.0 10.0 10.0
SFEC11
pede:      405.0 676.6 447.6 100.6 647.1 370.5 342.8 236.0 514.5 477.9
width:     1.5  1.4  1.2  1.4  1.4  0.9  0.9  0.9  0.9  0.9
thr:      10.0 10.0 10.0 10.0 10.0 10.0 10.0 10.0 10.0 10.0

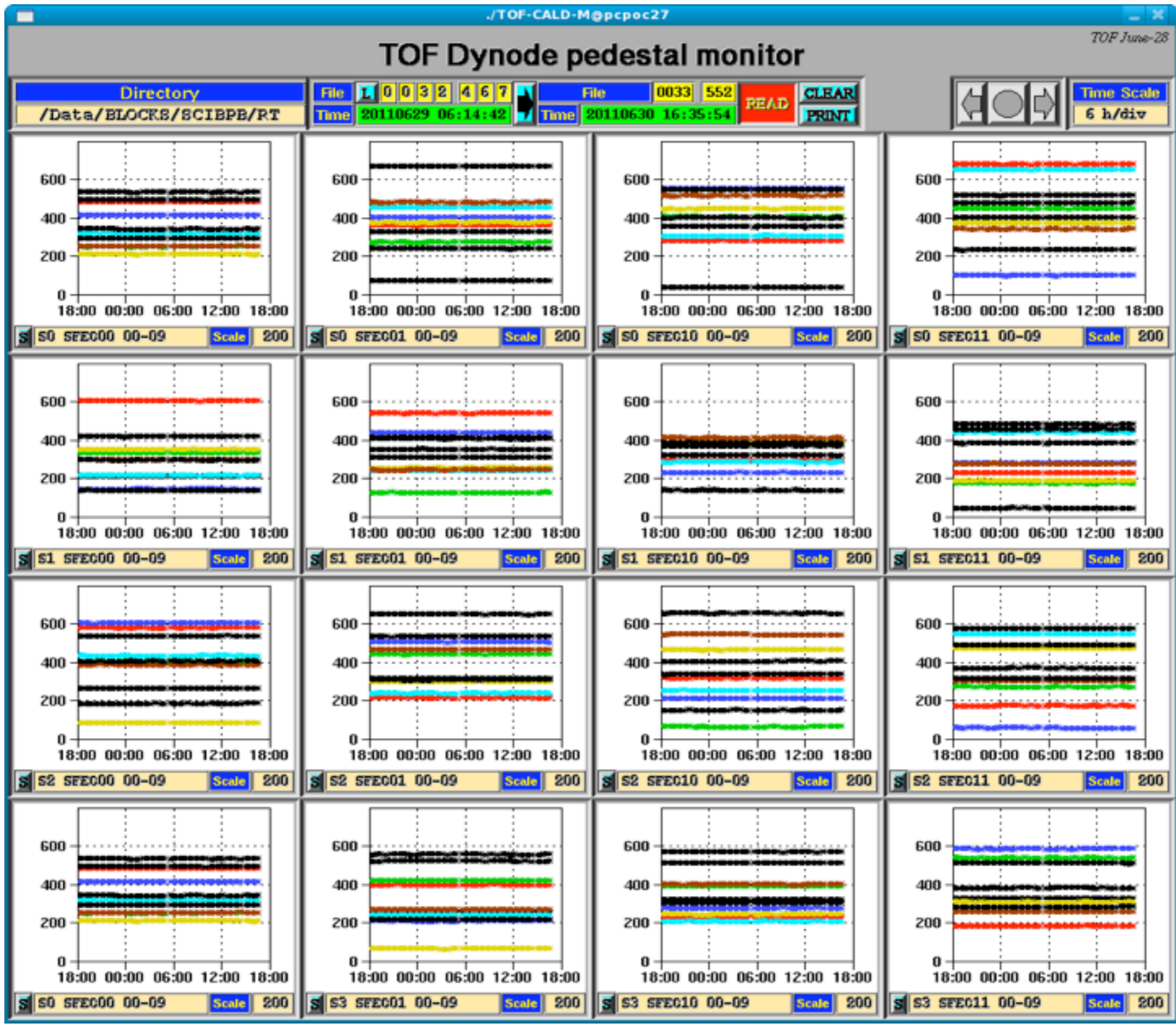
pretrigger: 1800 1800 0800 0800

status:     0000 1400 0000 1800 0000 0013 0032 7FFF 00B3 0000
```

TOF Pedestal Anode Monitor: TOF-CALA-M



TOF Dynode Pedestal Monitor: TOF-CALD-M



TOF configuration monitor

Configuration (data type 7)

All TOF electronics configuration parameters are retrieved before each run starts (every 30 minutes). The settings and read back values for S-crate and SHV bricks are written in a text file:

- 21 parameters for SDR2 board
- 161 parameters for SFET2A board
- 161 parameters for SFET2B board
- 10 parameters for SPT2 board
- 161 parameters for SFET2C board
- 161 parameters for SFET2D board
- 161 parameters for SFEA2 board
- 26 parameters for SHV brick

TOF configuration monitor

***** S1 crate *****

***** SDR2 parameters *****

Power monitor register
PMON register - power OFF: 0000 0000
PMON register - power ON: 7FFF 7FFF

Internal SDR2 register
SPT2 Command (H): 1400 1400
SPT2 Command (L): 0000 0000
SFET Command (H): 1800 1800
SFET Command (L): 0000 0000
Hold Time: 0013 0013
ADC Delay Time: 0032 0032
Poux Readout Time: 00B3 00B3
SFET Timeout: 01F4 01F4
Power Mask: 0000 0000
SFEC Clock Enable: 0001 0001
Programmable Busy: 2134 2134

Data Processing Control (DM)
Dynamic Pedestal Control: 0001 0001
Nsigma for SFET threshold: 0005 0005
Nsigma for SFEA threshold: 0003 0003
Nsigma for SFEC threshold: 0004 0004
Low limit on SFET threshold: 0030 0030
High limit on SFET threshold: 0078 0078
Low limit on SFEA threshold: 0060 0060
High limit on SFEA threshold: 00E0 00E0
Low limit on SFEC threshold: 0050 0050
High limit on SFEC threshold: 00A0 00A0

SDR2 init error code: 4000 4000

***** SPT2 parameters *****

Prescaler Subgroup
Prescaler gate: 0002 0002

Mask Subgroup
Mask for CP1: 0C00 0C00
Mask for CP0: 0C00 0C00
Mask for CT1: 0C00 0C00
Mask for CT0: 0C00 0C00
Mask for BZ1: 0C00 0C00
Mask for BZ0: 0C00 0C00

LVDS Driver Subgroup
LVDS driver Enable: 0001 0001

Pulser Subgroup
Pulser Control: 0000 0000
Pulser Period: 0000 0000
SPT2 Version: 0215 0215
SPT2 init error code: 0000 0000

***** SFET2A parameters *****

Threshold Subgroup
LT threshold, ch0-2: 8308 8308
LT threshold, ch3-4: 8708 8708
HT threshold, ch0-2: 830A 830A
HT threshold, ch3-4: 8713 8713
SHT threshold, ch0-2: 8350 8350
SHT threshold, ch3-4: 8778 8778

TDC Setup Subgroup
TEST SELECT: 000E 000E
ENABLE ERROR MARK: 0001 0001
ENABLE ERROR BYPASS: 0000 0000
ENABLE ERROR: 07FF 07FF
READOUT SC SPEED: 0002 0002
SERIAL DELAY: 0000 0000
STROBE SELECT: 0003 0003

....

SHV1

***** SHV parameters *****

DC/DC Subgroup
DC/DC power 0001 0001
DC/DC setting 03C1 03C1

LR Subgroup
LR0 setting 02E0 02E0
LR1 setting 02E0 02E0
LR2 setting 02E0 02E0
LR3 setting 035B 035B
LR4 setting 035B 035B
LR5 setting 02E0 02E0
LR6 setting 02E0 02E0
LR7 setting 0398 0398
LR8 setting 0398 0398
LR9 setting 031D 031D
LR10 setting 036F 036F

....

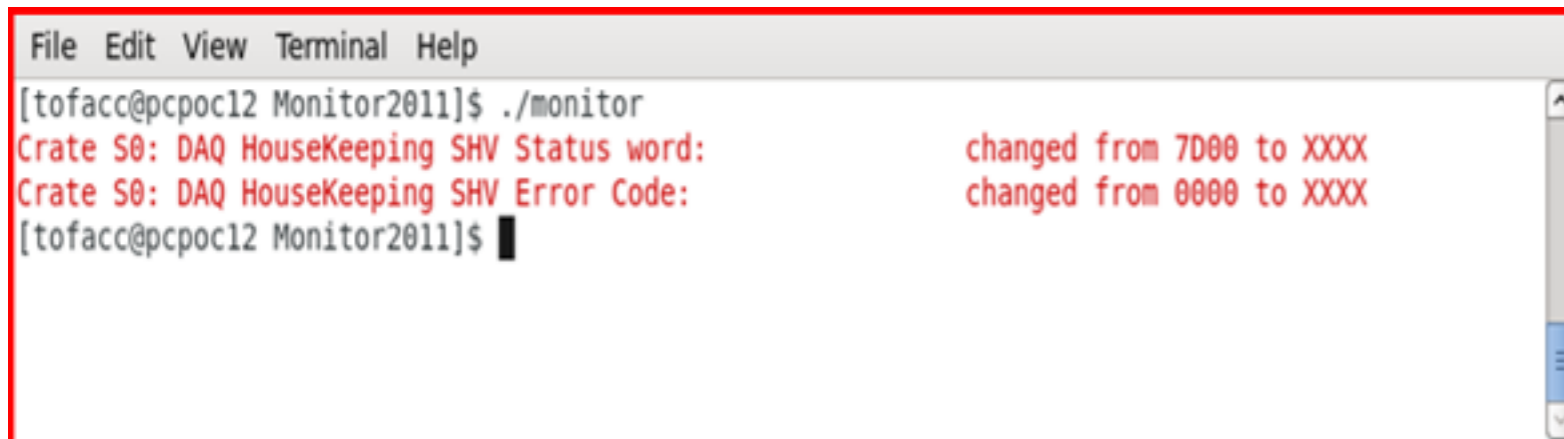
TOF house keeping

Housekeeping (data type 8)

Housekeeping information are checked constantly during data acquisition. HK words contain in brief the status of the detector and they can reveal if some parameter change in the TOF configuration.

All housekeeping information are saved in a text file.

In case of some housekeeping words change during DAQ, discrepancies are reported with red lines in the terminal window and errors are added to error file.



```
File Edit View Terminal Help
[tofacc@pcpoc12 Monitor2011]$ ./monitor
Crate S0: DAQ HouseKeeping SHV Status word:      changed from 7D00 to XXXX
Crate S0: DAQ HouseKeeping SHV Error Code:      changed from 0000 to XXXX
[tofacc@pcpoc12 Monitor2011]$
```

Program Version ID
Subdetector Version ID
Node status word
Last Event Number
Average Event Processing time
Calibration Type
Calibration Status
Power Monitor word
S-Crate Status word
SDR2 Error Code
SPT2 Error Code
SFET2-A Error Code
SFET2-B Error Code
SFET2-C Error Code
SFET2-D Error Code
SFEA2 Error Code
SHV Status word
SHV Error Code
Reply Status
Frame Check Sequence

TOF house keeping

Crate S0 - Housekeeping data

Program Version ID:	AB06
Subdetector Version ID:	AB08
Node Status word:	0000
Last Event Number:	0000
Average Event Processing time:	0004
Calibration Type:	8000
Calibration Status:	4000
Power Monitor word:	7FFF
S-Crate Status word:	C07F
SDR2 Error Code:	4000
SPT2 Error Code:	0000
SFET2A Error Code:	0000
SFET2B Error Code:	0000
SFET2C Error Code:	0000
SFET2D Error Code:	0000
SFEA2 Error Code:	0000
SHV Status word:	7D00
SHV Error Code:	0000
Reply Status:	00A0

TOF house keeping monitor

In standard situation:

DAQ Housekeeping Thu Jun 30 16:35:46 2011

	S0	S1	S2	S3
Node Status word	0000	0000	0000	0000
Calibration Status	4000	4000	4000	4000
Power Monitor word	7FFF	7FFF	7FFF	7FFF
S-Crate Status word	C07F	C07F	C07F	C07F
SDR2 Error Code	4000	4000	4000	4000
SPT2 Error Code	0000	0000	0000	0000
SFET2A Error Code	0000	0000	0000	0000
SFET2B Error Code	0000	0000	0000	0000
SFET2C Error Code	0000	0000	0000	0000
SFET2D Error Code	0000	0000	0000	0000
SFEA2 Error Code	0000	0000	0000	0000
SHV Status word	7D00	7D00	7D00	7D00
SHV Error Code	0000	0000	0000	0000

Waiting for a new file...

In case of errors:

tofac@pcpoc27:/nfs_mnt/pocchome/tofac/TOF/Monitor2010_hk

DAQ Housekeeping Mon Jun 27 09:45:58 2011

	S0	S1	S2	S3
Node Status word	0000	0000	0000	0000
Calibration Status	4000	4000	4000	4000
Power Monitor word	7FFF	7FFF	7FFF	7FFF
S-Crate Status word	FB7F Def: C07F	FB7F Def: C07F	FB7F Def: C07F	FB7F Def: C07F
SDR2 Error Code	0000 Def: 4000	0000 Def: 4000	0000 Def: 4000	0000 Def: 4000
SPT2 Error Code	0000	0000	0000	0000
SFET2A Error Code	0001 Def: 0000	0001 Def: 0000	0001 Def: 0000	0001 Def: 0000
SFET2B Error Code	0001 Def: 0000	0001 Def: 0000	0001 Def: 0000	0001 Def: 0000
SFET2C Error Code	0001 Def: 0000	0001 Def: 0000	0001 Def: 0000	0001 Def: 0000
SFET2D Error Code	0001 Def: 0000	0001 Def: 0000	0001 Def: 0000	0001 Def: 0000
SFEA2 Error Code	0001 Def: 0000	0001 Def: 0000	0001 Def: 0000	0001 Def: 0000
SHV Status word	7D00	7D00	7D00	7D00
SHV Error Code	0000	0000	0000	0000

^C

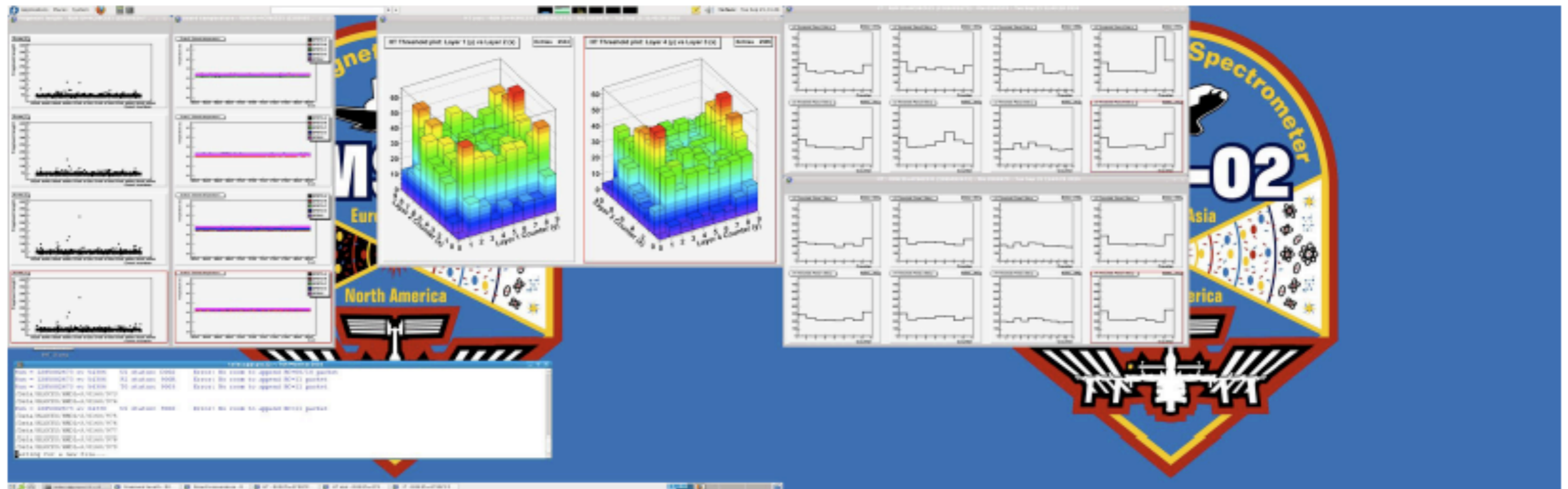
TOF online monitor: physics events

Physics events (data type 5)

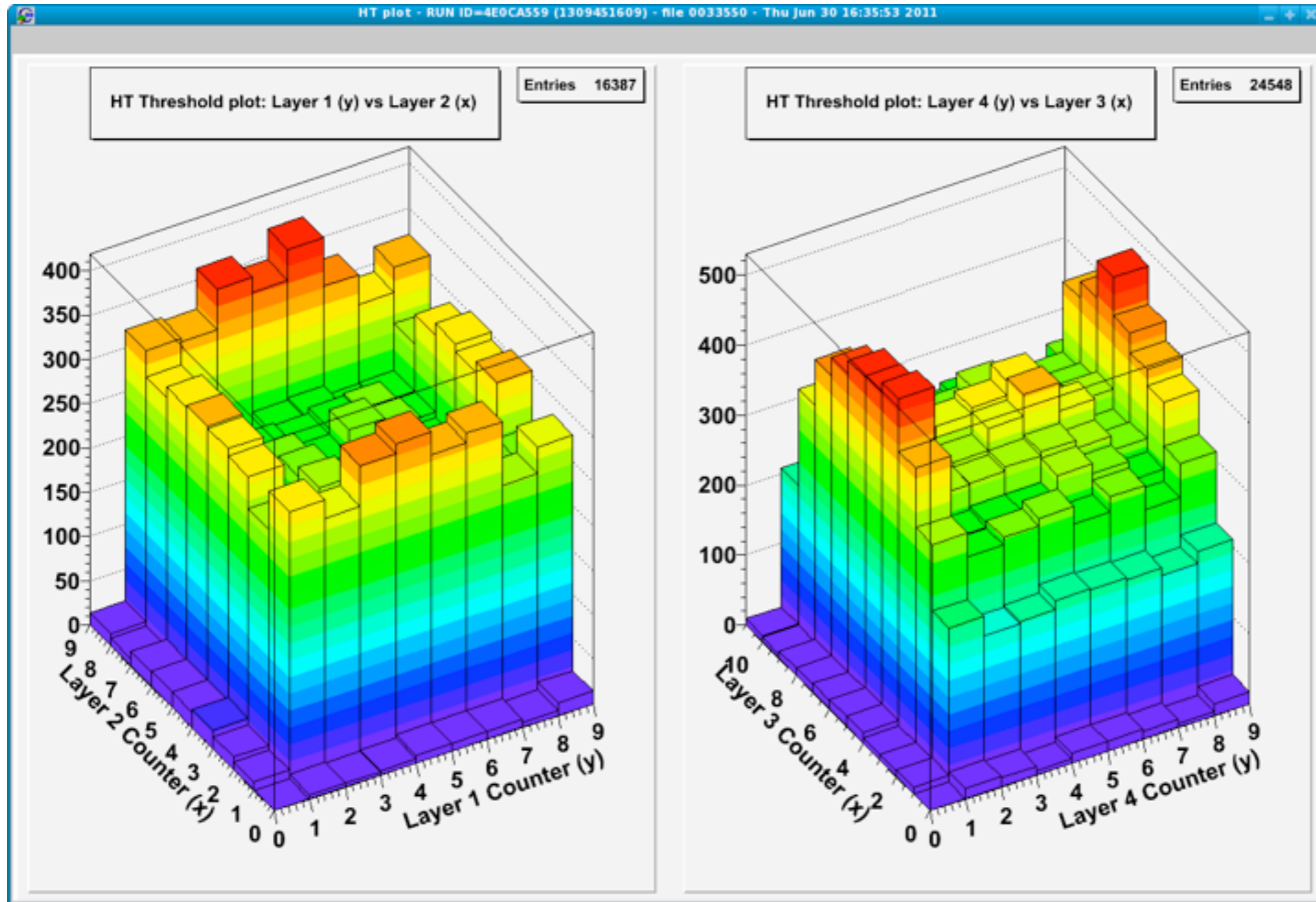
Physics events for TOF are represented using some significant plots that summarize the detector behavior. The plots visualized by TOF online monitor are:

- S-crate event size;
- SFET2/SFEA2 board temperatures;
- low thresholds/TDC number of hits;
- high thresholds/trigger rates;
- trigger occupancy histogram.

In each plot, run number, data file and the date are reported.



TOF online monitor: HT thresholds

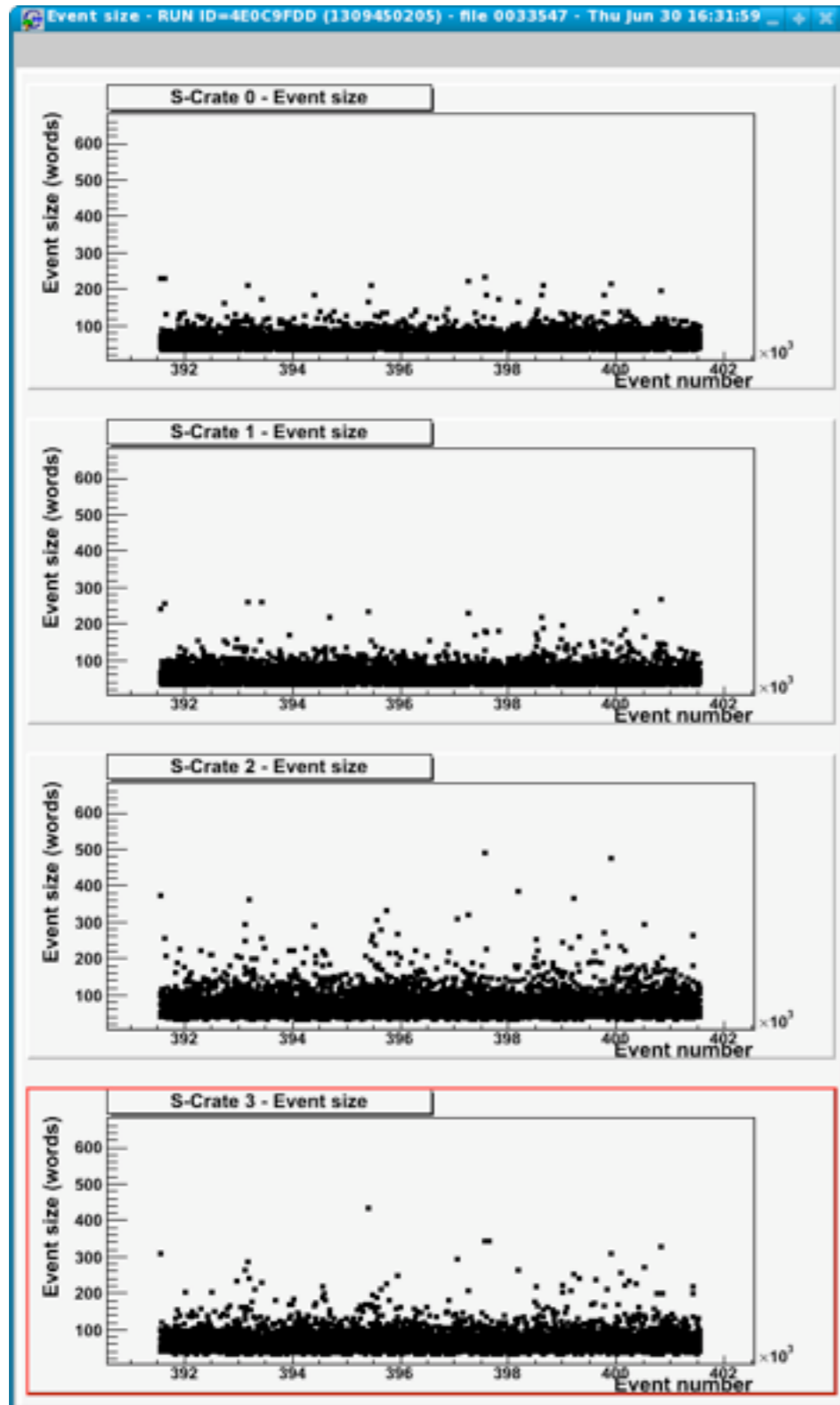


These two histograms gave you a global view that the TOF is triggering, in particular they show the number of times each TOF counter gave a trigger. In the left the histogram for the Upper TOF counters in the first and the second layer in the y and x axis respectively is shown while in the right is shown the LTOF one.

TOF online monitor: Event size

Event size

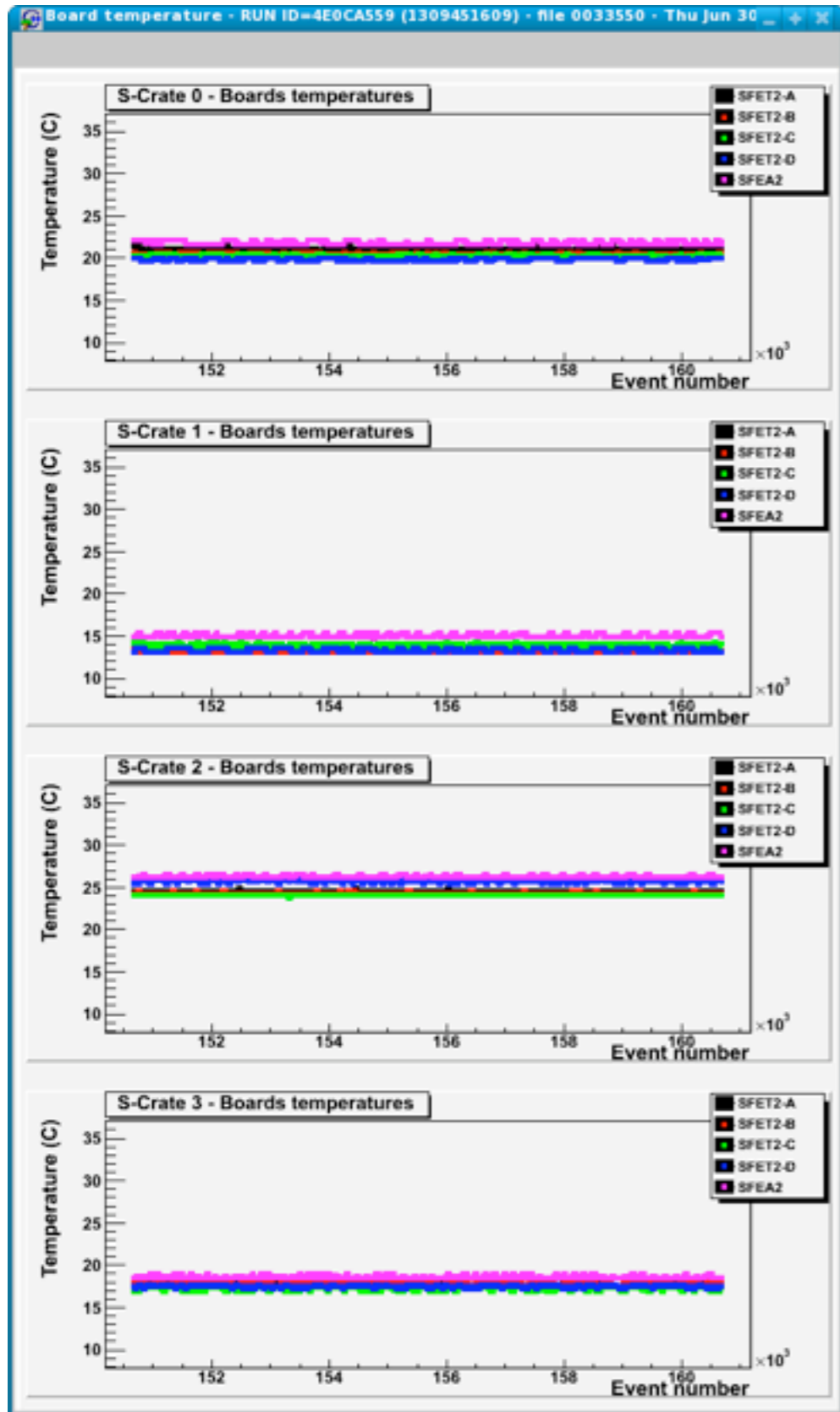
The S-crate event size is the first word of the SDR event fragment. It is a sum of charge, pre-trigger, status and time words.



The Event size (in number of words) versus the event number for each of the four TOF crates (Crate S0/S1/S2/S3) is shown. The crate are connected to TOF layers and sides with this scheme:

S0: 1n 2p
S1: 1p 2n
S2: 3p 4p
S3: 3n 4n

TOF online monitor: Event size

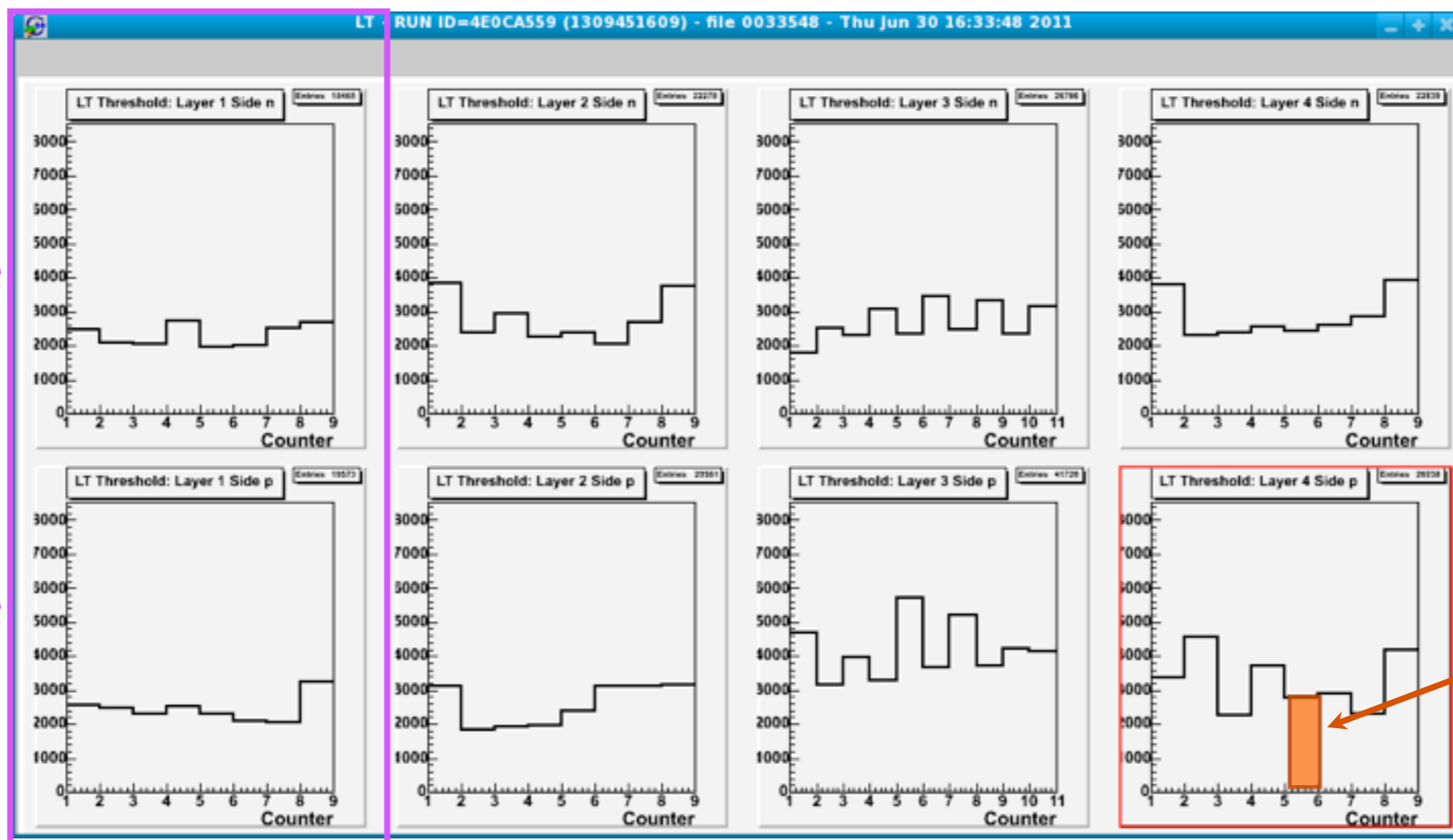


The electronic board temperatures versus the number of event for each of the four TOF crates is monitored.

TOF low thresholds

TOP:
negative
counters
side (n)

Bottom:
positive
counters
side (p)



Layer 4
Counter 5
positive side (p)

These histograms show the number of times that the signal cross the low thresholds used for the time measurements (number of hits in the TDC).

From left to right each couple of histograms (top and bottom) represent respectively the first, the second, the third and the fourth TOF layer.

In each histogram the number of hits measured in each TOF counter are shown.

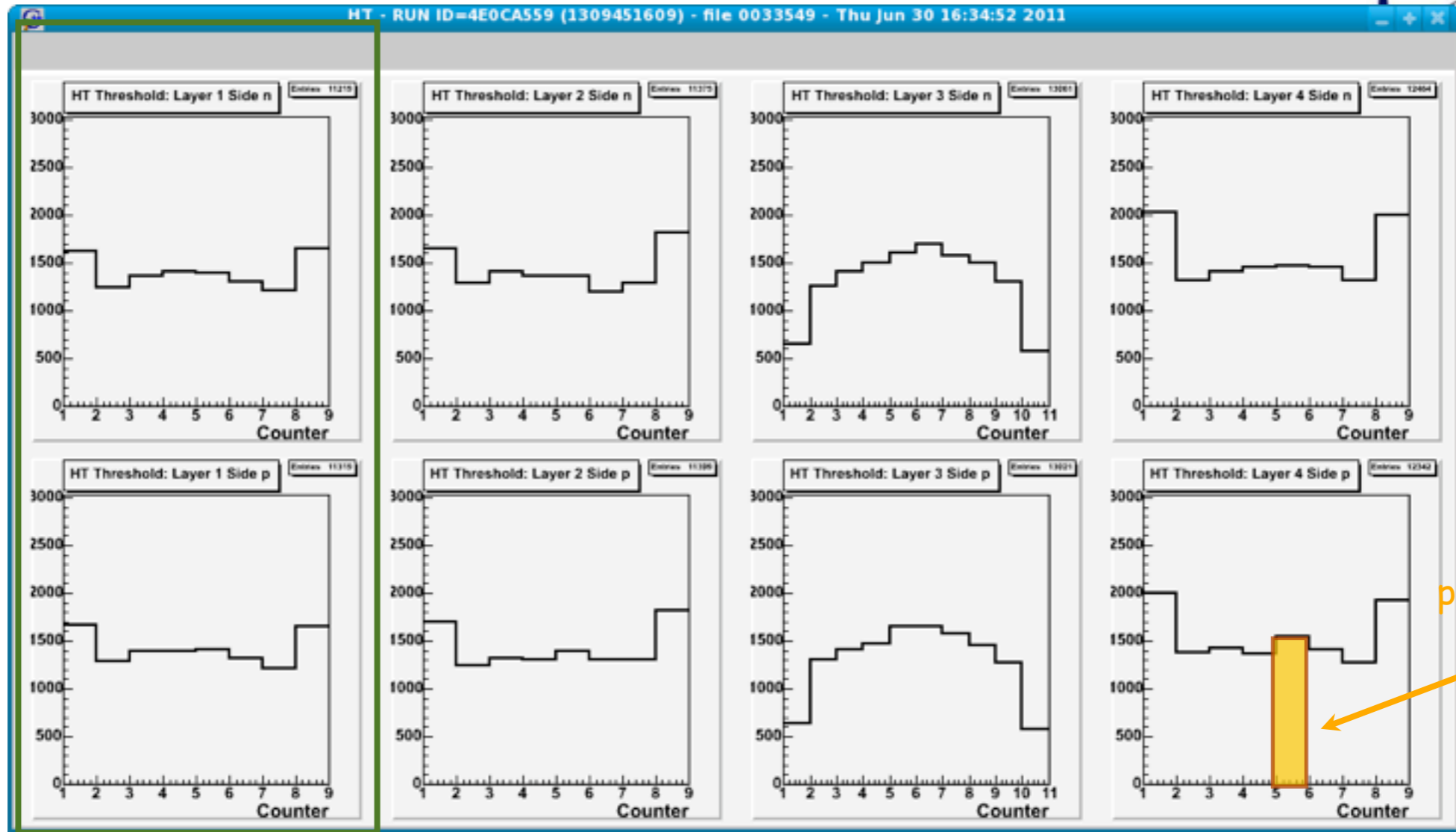


TOF high thresholds



TOP:
negative
counters
side (n)

Bottom:
positive
counters
side (p)



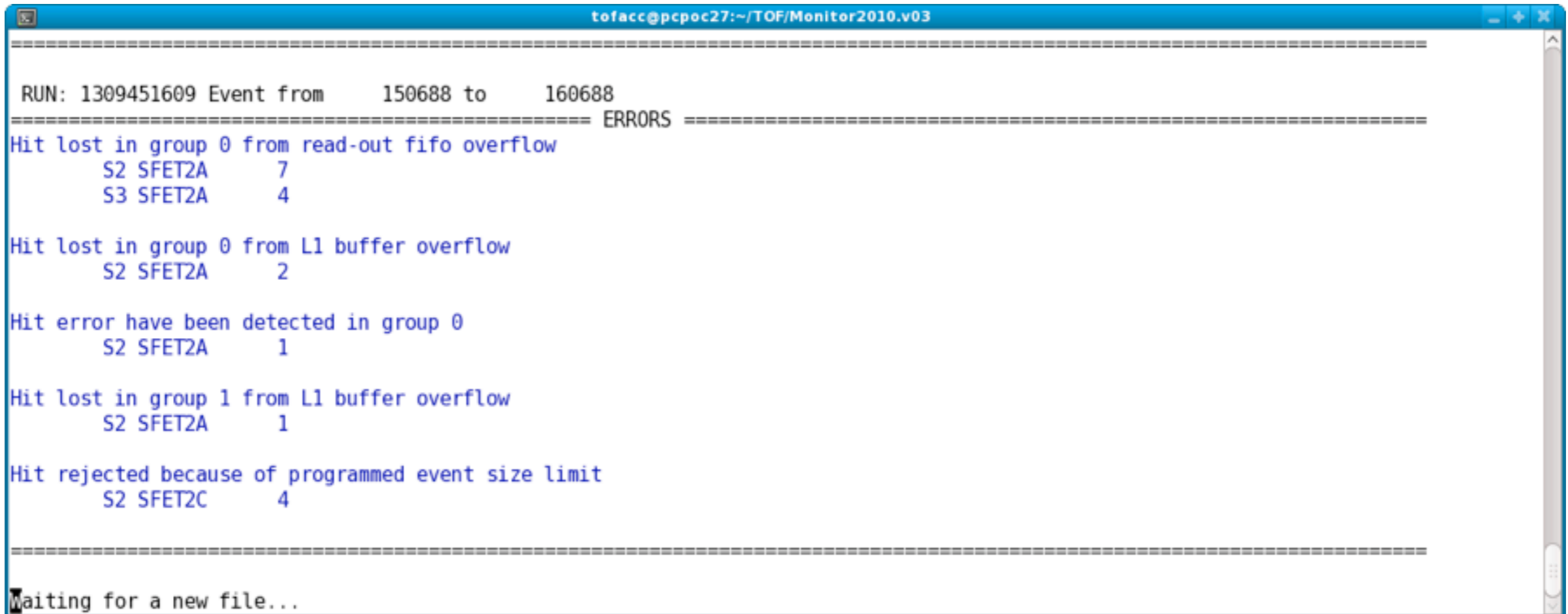
These histograms show the number of times that the signal cross the high thresholds used to generate the AMS trigger.

In each histogram the number of triggers from each TOF counter are shown.

The first and last counter of each plot give always more counts because of geometrical reasons (their trapezoidal, with a larger surface), except for layer 3, which has 10 counters, because counters 1 and 10 are removed from the physical trigger. This was done to have an acceptance conform to the tracker.

TOF blue errors

While monitor is running some errors could occur, they are highlighted in colored lines. The errors related to the DAQ are written in blue as shown below:



```
tofac@pcpoc27:~/TOF/Monitor2010.v03
=====
RUN: 1309451609 Event from 150688 to 160688
===== ERRORS =====
Hit lost in group 0 from read-out fifo overflow
  S2 SFET2A 7
  S3 SFET2A 4
Hit lost in group 0 from L1 buffer overflow
  S2 SFET2A 2
Hit error have been detected in group 0
  S2 SFET2A 1
Hit lost in group 1 from L1 buffer overflow
  S2 SFET2A 1
Hit rejected because of programmed event size limit
  S2 SFET2C 4
=====
Waiting for a new file...
```

Errors found during the data acquisition can be:

- DAQ errors (error reading or building data blocks...);
- Node status errors;
- Configuration parameters discrepancies;
- TDC errors;
- Housekeeping discrepancies.

TOF red errors

The errors strictly related to the TOF are written in red, and they are spotted as shown below.

```
Monitor2010 : monitor
File Edit View Scrollback Bookmarks Settings Help
Run = 1307305115 ev 274003 S2 SFET2C Hit rejected because of programmed event size limit
Run = 1307305115 ev 274003 S2 SFET2A Error flag = 0
writing DAQ housekeeping data into file: /nfs_mnt/pocchome/tofacc/TOF/Monitor2010/daqhk/1307305633.
hk
writing errors into file: /nfs_mnt/pocchome/tofacc/TOF/Monitor2010/err/1307305633.err
Crate S0: DAQ HouseKeeping S-Crate Status word: changed from C07F to FB7F
Crate S0: DAQ HouseKeeping SFET2A Error Code: changed from 0000 to 0001
Crate S0: DAQ HouseKeeping SFET2B Error Code: changed from 0000 to 0001
Crate S0: DAQ HouseKeeping SFET2C Error Code: changed from 0000 to 0001
Crate S0: DAQ HouseKeeping SFET2D Error Code: changed from 0000 to 0001
Crate S0: DAQ HouseKeeping SFEA2 Error Code: changed from 0000 to 0001
Crate S1: DAQ HouseKeeping S-Crate Status word: changed from C07F to FB7F
Crate S1: DAQ HouseKeeping SFET2A Error Code: changed from 0000 to 0001
Crate S1: DAQ HouseKeeping SFET2B Error Code: changed from 0000 to 0001
Crate S1: DAQ HouseKeeping SFET2C Error Code: changed from 0000 to 0001
Crate S1: DAQ HouseKeeping SFET2D Error Code: changed from 0000 to 0001
Crate S1: DAQ HouseKeeping SFEA2 Error Code: changed from 0000 to 0001
Crate S2: DAQ HouseKeeping S-Crate Status word: changed from C07F to FB7F
Crate S2: DAQ HouseKeeping SFET2A Error Code: changed from 0000 to 0001
Crate S2: DAQ HouseKeeping SFET2B Error Code: changed from 0000 to 0001
Crate S2: DAQ HouseKeeping SFET2C Error Code: changed from 0000 to 0001
Crate S2: DAQ HouseKeeping SFET2D Error Code: changed from 0000 to 0001
Crate S2: DAQ HouseKeeping SFEA2 Error Code: changed from 0000 to 0001
Crate S2: DAQ HouseKeeping SHV Error Code: changed from 0000 to 0001
Crate S3: DAQ HouseKeeping S-Crate Status word: changed from C07F to FB7F
Crate S3: DAQ HouseKeeping SFET2A Error Code: changed from 0000 to 0001
Crate S3: DAQ HouseKeeping SFET2B Error Code: changed from 0000 to 0001
Crate S3: DAQ HouseKeeping SFET2C Error Code: changed from 0000 to 0001
Crate S3: DAQ HouseKeeping SFET2D Error Code: changed from 0000 to 0001
Crate S3: DAQ HouseKeeping SFEA2 Error Code: changed from 0000 to 0001
Empty ERROR file removed.
writing errors into file: /nfs_mnt/pocchome/tofacc/TOF/Monitor2010/err/1307305984.err
Empty ERROR file removed.
writing errors into file: /nfs_mnt/pocchome/tofacc/TOF/Monitor2010/err/1307305985.err
Run = 1307305985 ev 274185 S3 SFET2A Hit lost in group 0 from read-out fifo overflow
writing errors into file: /nfs_mnt/pocchome/tofacc/TOF/Monitor2010/err/1307305987.err
Empty ERROR file removed.
```

TOF Slow control Monitor: TOF-DTS

MIN Operative °C	MAX Operative °C	Normal °C
-30	+35	15 ÷ 16

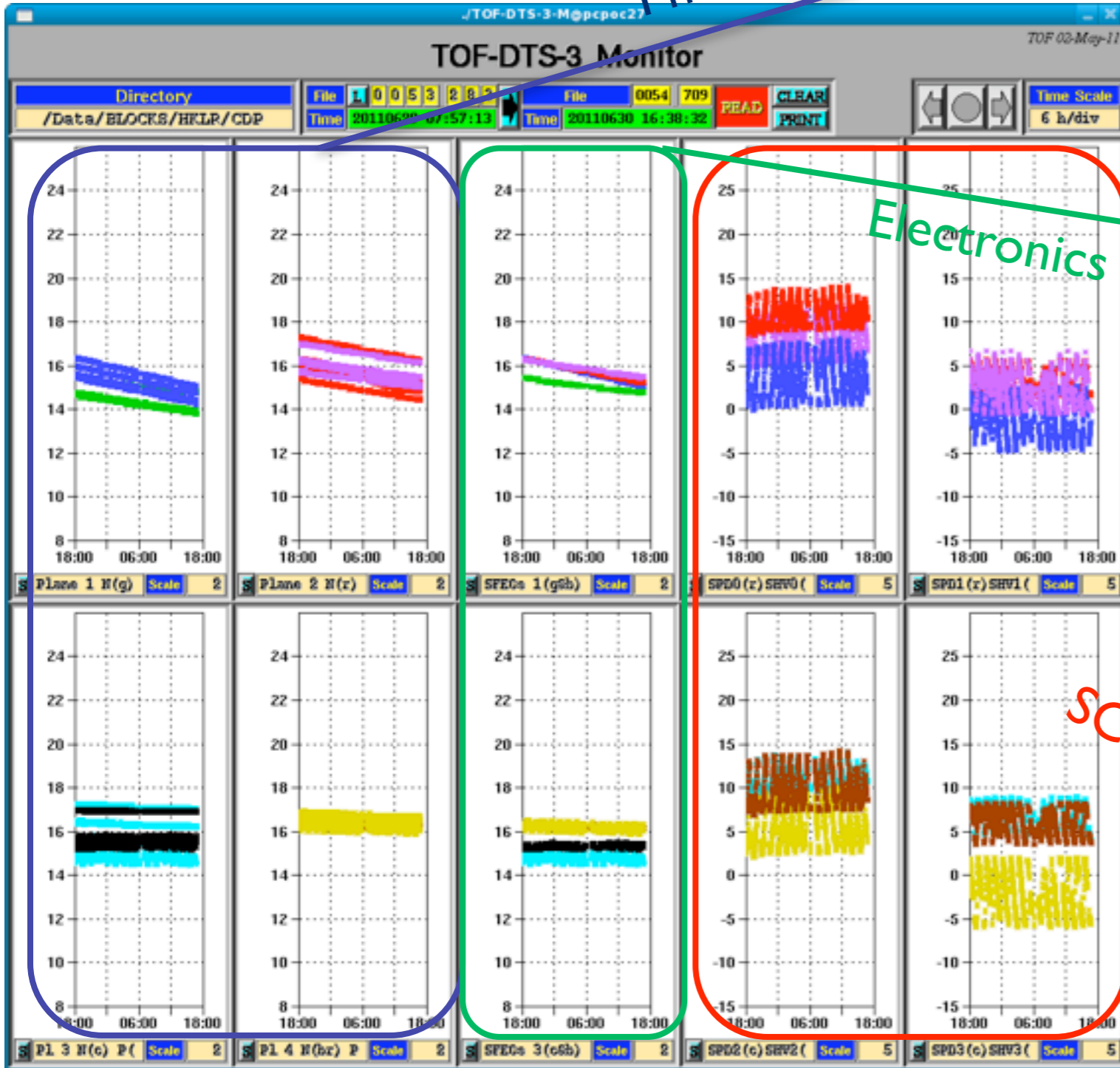
Phototubes

Electronics

SCrate

	MIN Operative °C	MAX Operative °C	Normal °C
SFEC	-40	+80	15 ÷ 16

MIN Operative °C	MAX Operative °C	Normal °C
-20	+50	-5 ÷ 15



TOF Slow control Monitor: DTS

TOF temperatures are monitored also using GTSN-3-M program. This program shows current values of temperatures and alarm status.



The screenshot shows a window titled "UTOF" containing a table with columns for "UTOF", "A", and "B". The table lists various TOF units and their corresponding temperature values. The "A" column is mostly empty, and the "B" column shows temperature values in degrees Celsius, some with a positive sign. The units are grouped by location: M-2, M-3, J-6, and J-5.

	UTOF	A	B
M-2	00:TOF-1 SFEC_00		+22.8
M-2	01:TOF-1 106n1		+22.1
M-2	02:TOF-1 104n1		+22.2
M-2	03:TOF-1 102n1		+21.9
M-2	04:TOF-1 108p2		+21.8
M-2	05:TOF-1 106p2		+22.2
M-2	06:TOF-1 104p2		+22.4
M-2	07:TOF-1 SFEC_10		+22.7
M-2	08:TOF-2 208n2		+21.9
M-2	09:TOF-2 SFEC_11		+21.9
M-2	10:TOF-2 204n1		+22.1
M-2	11:TOF-2 201n1		+21.8
M-2	12:TOF-2 208p2		+21.8
M-2	13:TOF-2 204p1		+21.9
M-2	14:TOF-2 SFEC_01		+21.9
M-2	15:TOF-2 201p1		+21.8
M-3	05:ACC+ZRam		+21.1
M-3	14:ACC+ZWake		+21.5
J-6	13:SPD0		+25.4
J-6	08:S0		+26.6
J-6	12:SHV0		+22.4
J-5	06:SPD1		+25.9
J-5	11:S1		+25.4
J-5	07:SHV1		+22.3

TOF monitor for the slow rate data

SPT2 scaler [Hz] Thu Jun 30 16:41:27 2011

+-Lay 1N	HT	SHT	+-Lay 1P	HT	SHT	+-Lay 2N	HT	SHT	+-Lay 2P	HT	SHT	+-Lay 3N	HT	SHT	+-Lay 3P	HT	SHT	+-Lay 4N	HT	SHT	+-Lay 4P	HT	SHT
1n	3772	903	1p	3769	977	1n	3724	1022	1p	3827	1114	1n	3184	1012	1p	3066	933	1n	4754	1214	1p	4686	1229
2n	2388	565	2p	2516	762	2n	2624	1220	2p	2447	697	2n	2469	632	2p	2697	1160	2n	2756	727	2p	2930	1183
3n	2612	805	3p	2754	1205	3n	2837	1268	3p	2452	598	3n	2720	822	3p	2705	900	3n	2799	770	3p	2907	1042
4n	2682	1146	4p	2486	755	4n	2641	985	4p	2522	598	4n	2906	789	4p	2857	822	4n	3081	1242	4p	2831	837
5n	2466	686	5p	2510	966	5n	2635	884	5p	2773	1111	5n	2860	747	5p	3206	1411	5n	2770	760	5p	3206	1707
6n	2477	659	6p	2483	647	6n	2338	594	6p	2829	1103	6n	3085	1064	6p	2899	773	6n	2929	980	6p	2768	733
7n	2406	630	7p	2375	687	7n	2410	708	7p	2548	909	7n	2780	665	7p	2812	828	7n	2797	1029	7p	2678	607
8n	3716	889	8p	3683	833	8n	3992	1283	8p	4074	1404	8n	2840	790	8p	2730	768	8n	4695	1198	8p	4613	1235
												9n	2659	934	9p	2422	693						
												10n	3163	801	10p	3137	999						

Dallas Sensor Temperatures [°C] Thu Jun 30 16:40:53 2011

TOF-1 SFEC_00	14.8	TOF-2 208n2	14.9	TOF-3 SFEC_30	14.6	TOF-4 SFEC_31	16.2
TOF-1 106n1	13.9	TOF-2 SFEC_11	15.2	TOF-3 302n1	15.5	TOF-4 402n2	16.6
TOF-1 104n1	13.9	TOF-2 204n1	16.2	TOF-3 305n2	17.0	TOF-4 404n2	16.2
TOF-1 102n1	13.8	TOF-2 201n1	14.4	TOF-3 309n2	16.2	TOF-4 406n2	16.4
TOF-1 108p2	14.3	TOF-2 208p2	15.2	TOF-3 301p2	15.5	TOF-4 401p1	16.3
TOF-1 106p2	14.4	TOF-2 204p1	16.1	TOF-3 305p2	16.9	TOF-4 404p1	16.5
TOF-1 104p2	14.8	TOF-2 SFEC_01	15.5	TOF-3 309p2	15.8	TOF-4 406p1	16.7
TOF-1 SFEC_10	15.1	TOF-2 201p1	15.1	TOF-3 SFEC_20	15.3	TOF-4 SFEC_21	16.2
SHV0	1.7	SHV1	-1.4	SHV2	3.9	SHV3	-5.6
SPD0 (TSPD1)	10.3	SPD1 (TSPD3)	1.6	SPD2 (TSPD4)	10.5	SPD3 (TSPD6)	3.9

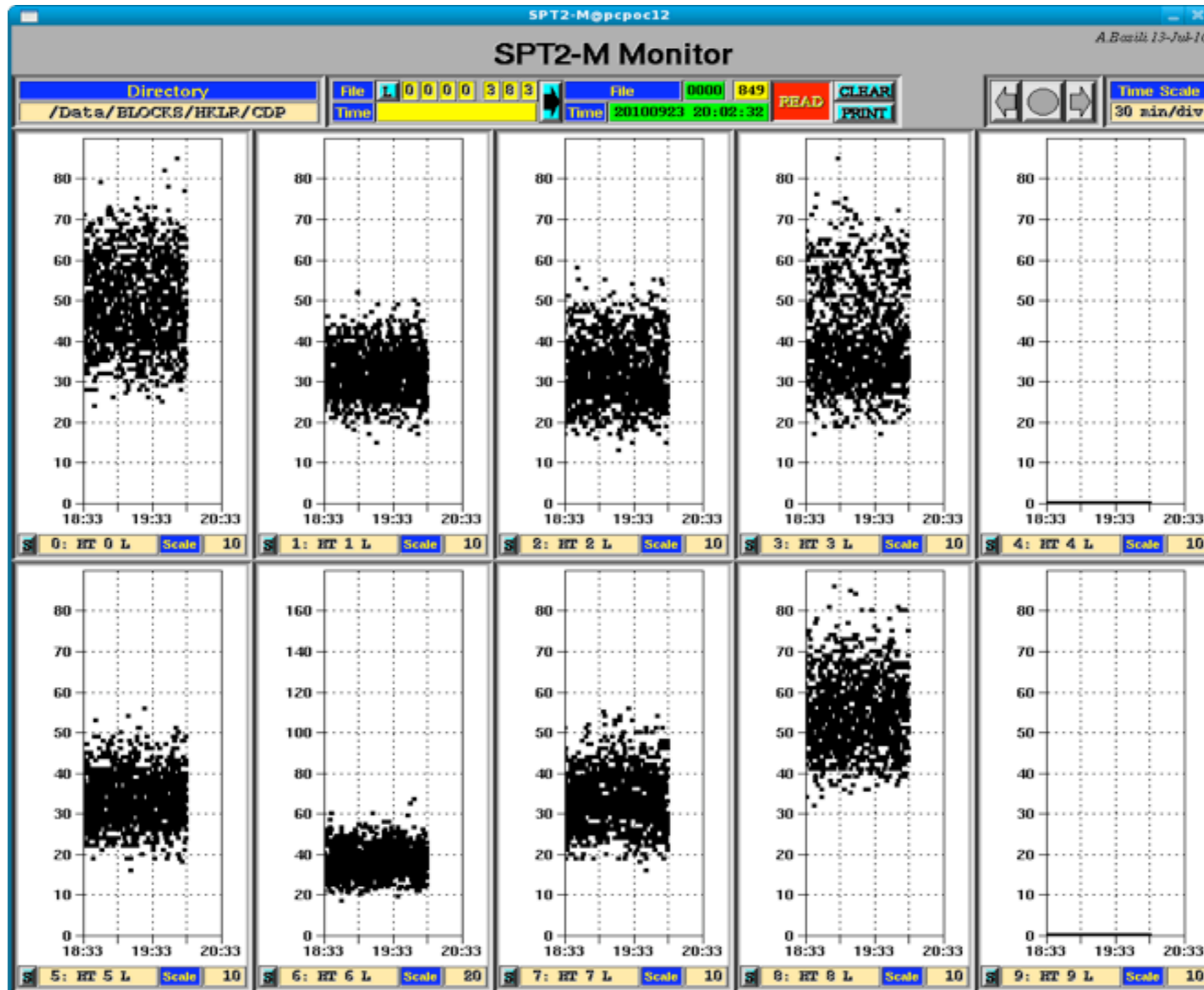
DAQ Housekeeping Thu Jun 30 16:35:46 2011

	S0	S1	S2	S3
Node Status word	0000	0000	0000	0000
Calibration Status	4000	4000	4000	4000
Power Monitor word	7FFF	7FFF	7FFF	7FFF
S-Crate Status word	C07F	C07F	C07F	C07F
SDR2 Error Code	4000	4000	4000	4000
SPT2 Error Code	0000	0000	0000	0000
SFET2A Error Code	0000	0000	0000	0000
SFET2B Error Code	0000	0000	0000	0000
SFET2C Error Code	0000	0000	0000	0000
SFET2D Error Code	0000	0000	0000	0000
SFEA2 Error Code	0000	0000	0000	0000
SHV Status word	7D00	7D00	7D00	7D00
SHV Error Code	0000	0000	0000	0000

Waiting for a new file...

TOF Slow control Monitor: SPT-M

Pre-trigger signals, generated by Time Of Flight in the SPT2 board, are monitored by SPT2-M program. This program is useful when DAQ is not running to check that TOF pre-triggers are generated. The OR of all pre-trigger signals (HT), coming from one side of each TOF plane, produce a CP signal in input to JLV1 board.



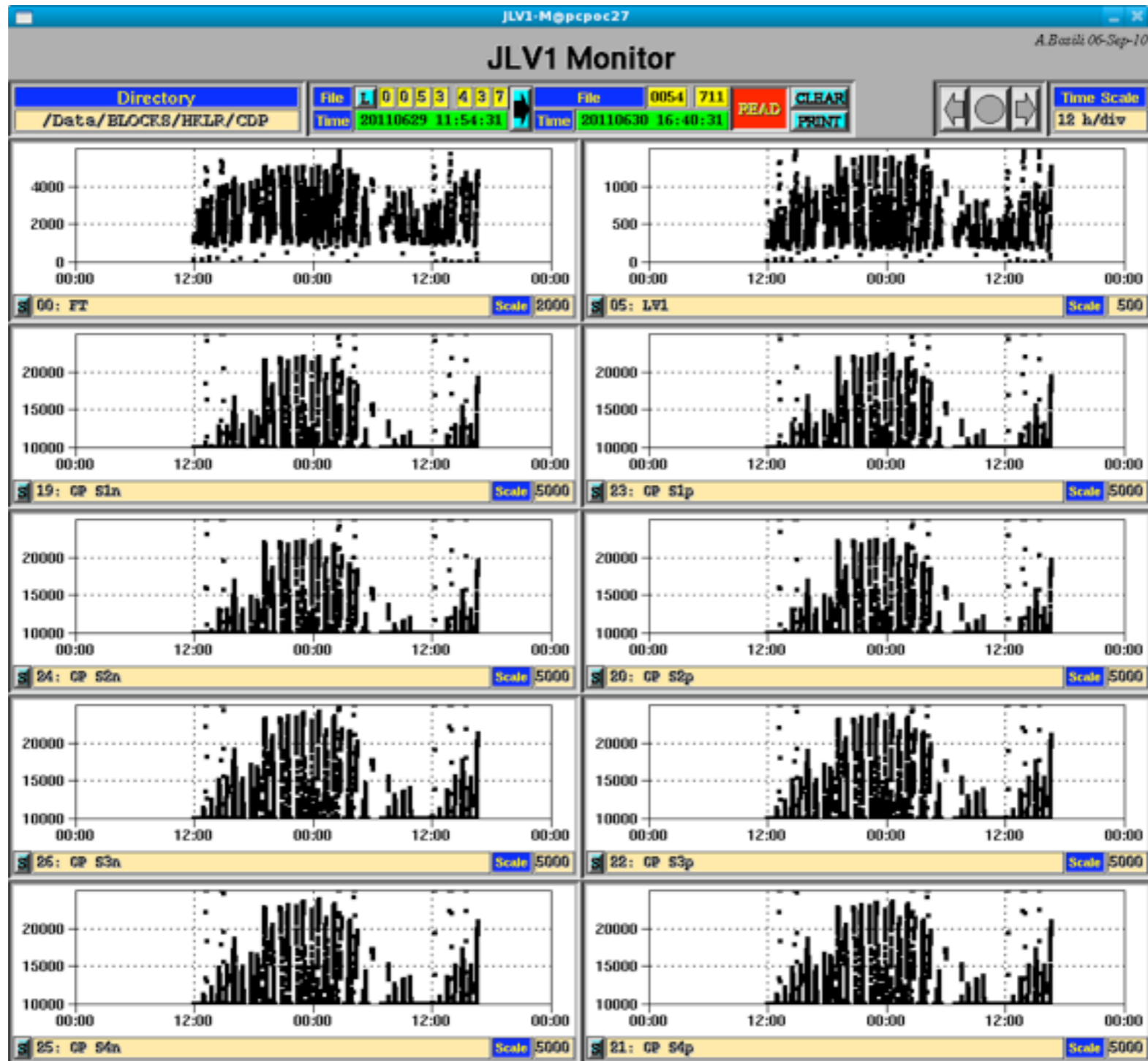
TOF Slow control Monitor: JLV1

The JLV1 monitor shows the FT, the LV1 and the TOF inputs to the trigger box.

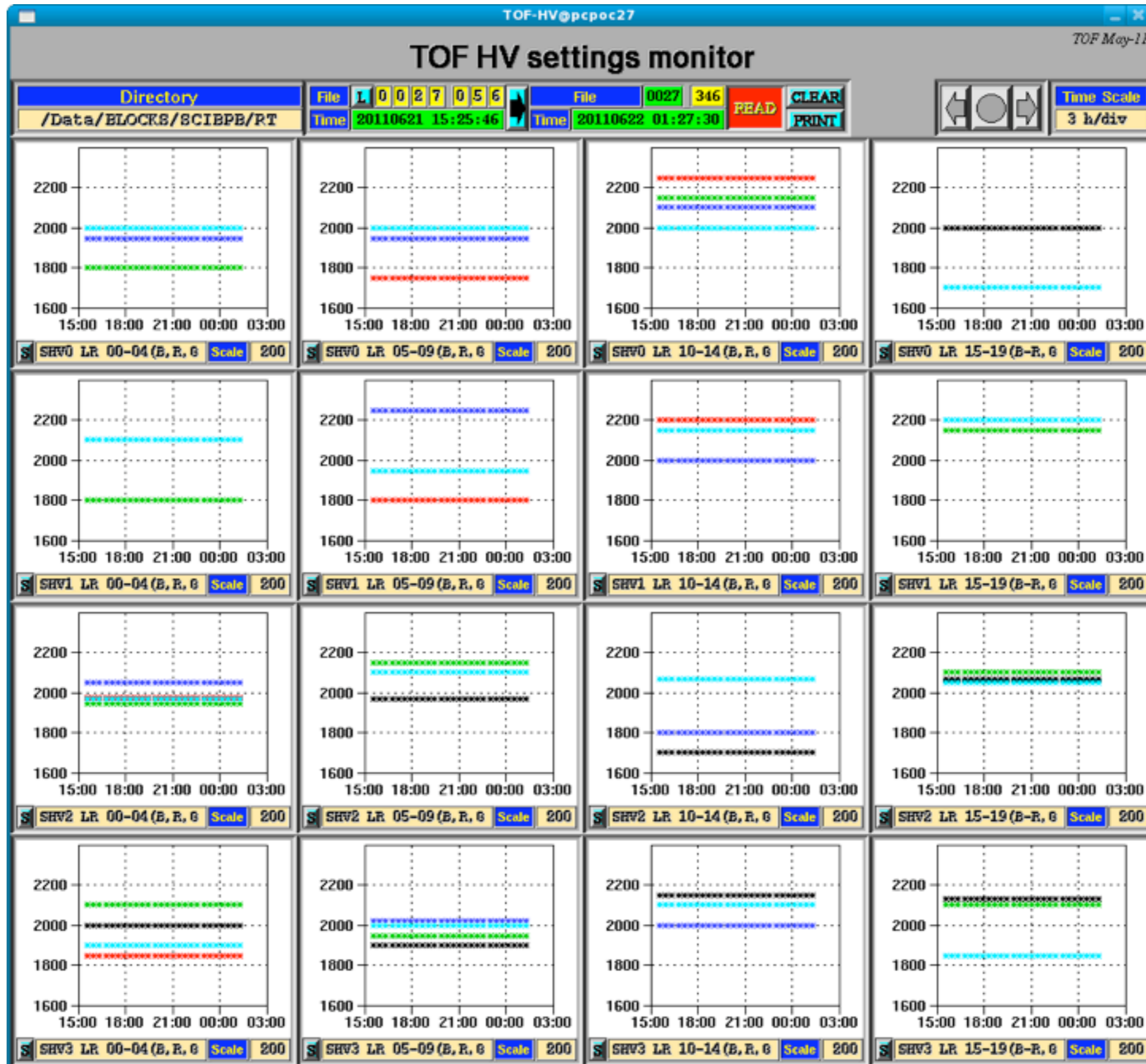
FT - AMS fast trigger

LV1 - AMS LV1 trigger

CP S1n, CP S2n, ... CP S3p, CP S4p are the TOF signals going in inputs to the trigger board (JLV1) for the FT generation.



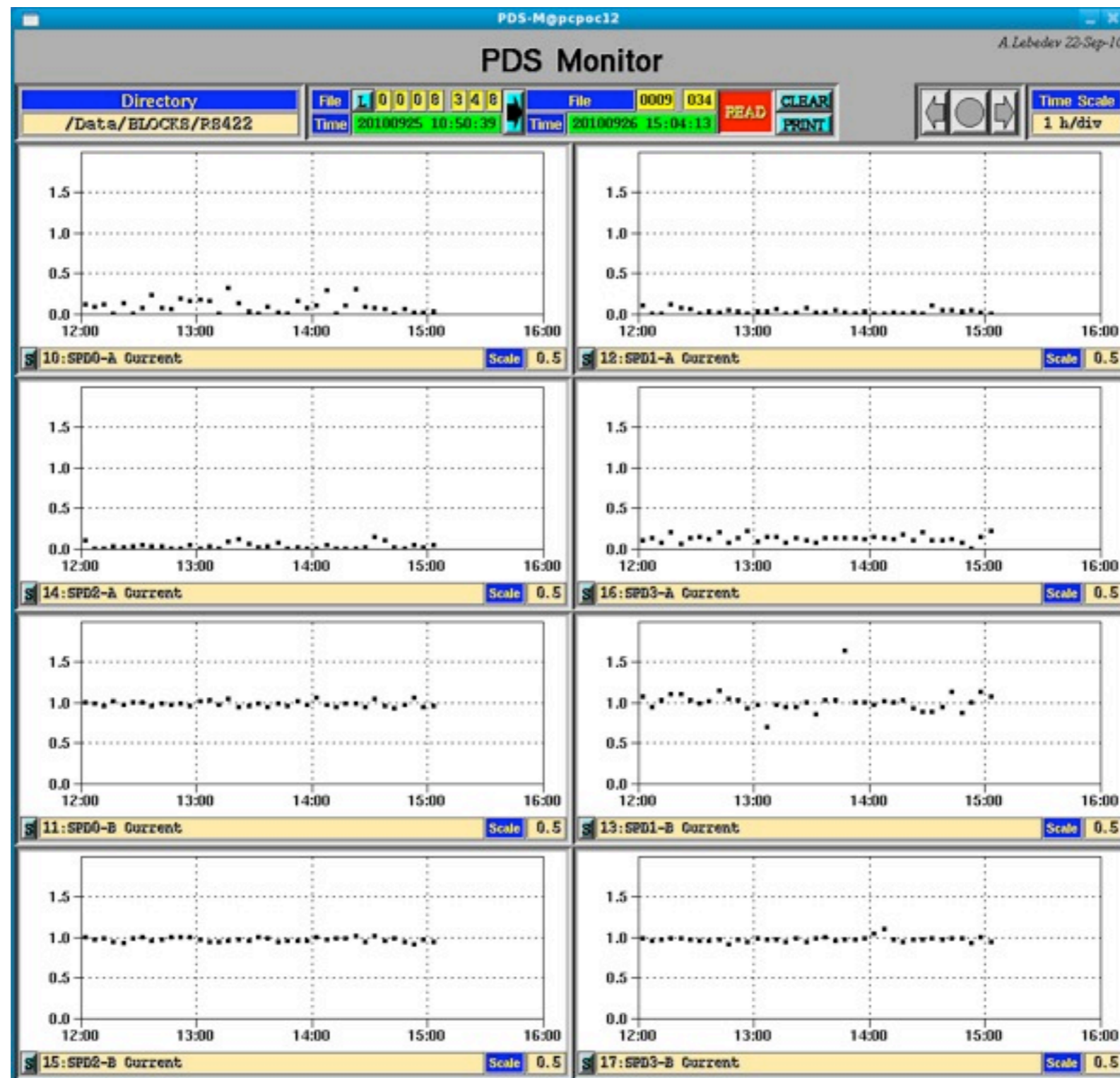
TOF Slow control Monitor: HV settings



TOF Slow control Monitor: PDS monitor

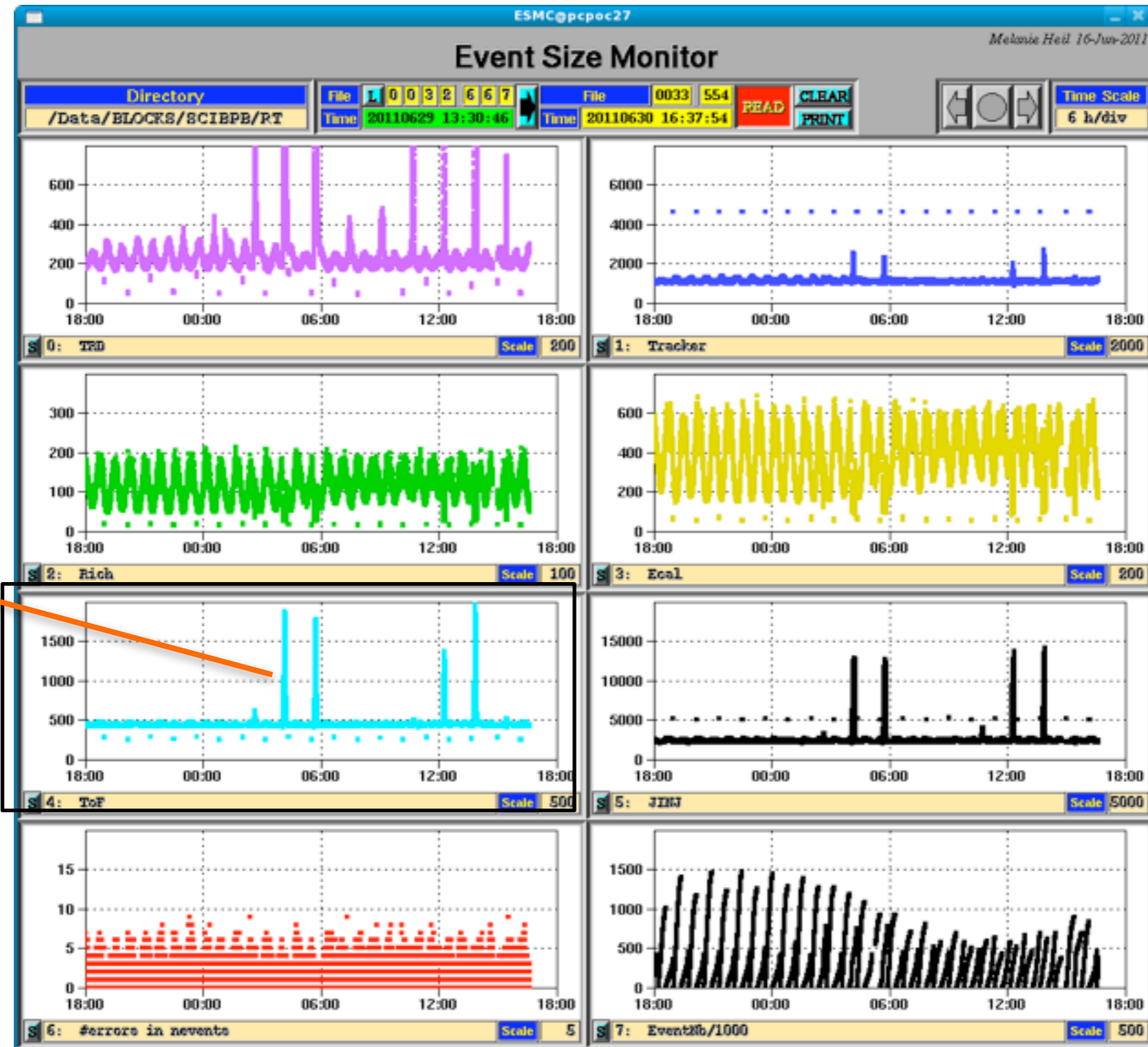
Time Of Flight crates and high voltage bricks are powered by 4 SPDs.

When TOF is powered and photomultiplier voltages ramp up at the standard values, the consumption of each SPD is about 1 A.



Event size Monitor:TOF

This is a general monitor, also used by the lead, to control the size of the events for each sub-detector.



The peaks are due to the South Atlantic Anomaly.

